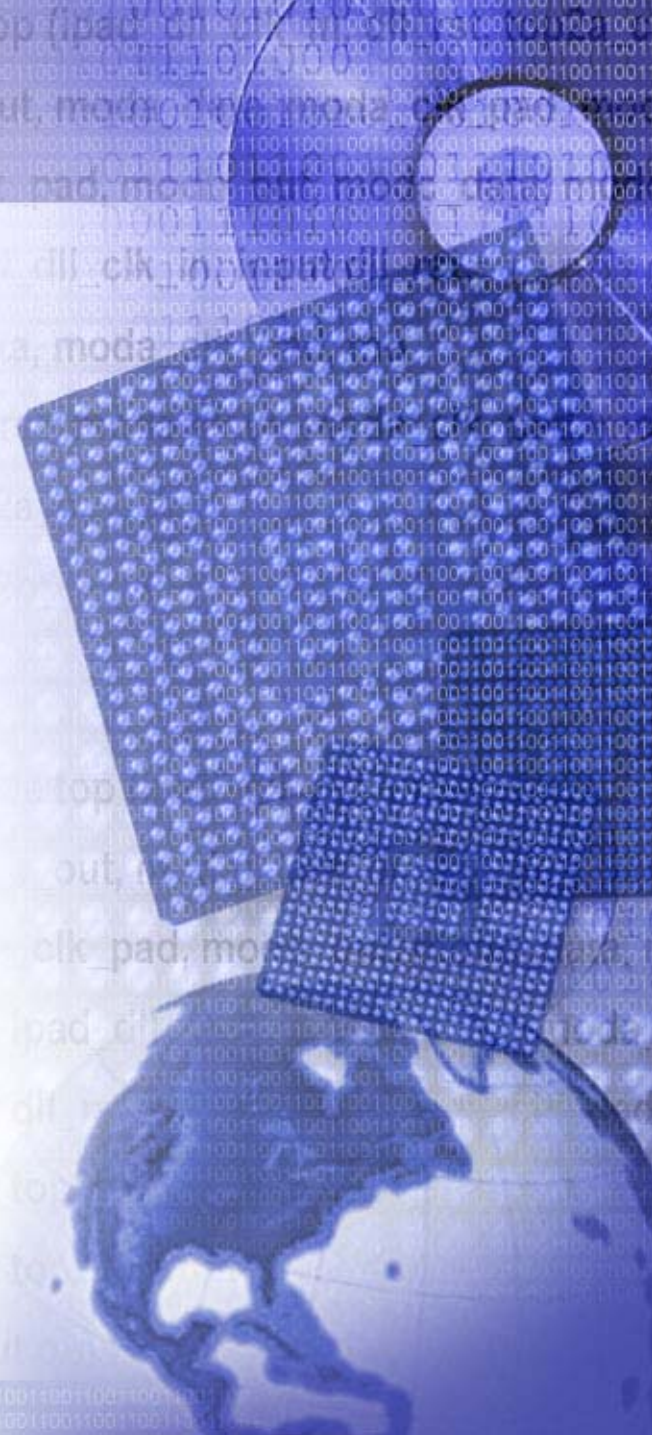




Customer benefits of Platform Cable USB Features

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Feature Explanations

- Bus-powered (no external power supply required)
 - *Simple host connection*
 - *Voltage and current margins are guaranteed*
- Selectable target clock frequency up to 24 MHz
 - *Match cable performance with capabilities of target device*
 - *Significant reduction in test time for repetitive batch mode operations*
- USB-IF certified
 - *Guaranteed not to interfere with operation of other USB devices*
- Compatible with Full-Speed and Hi-Speed USB ports
 - *No switch settings or hardware configuration required*
 - *Works with a wide variety of host platforms and external hubs*
 - *Superior performance with Hi-Speed USB ports*
- Auto update of cable firmware with each new install of ISE service pack
 - *Performance improvements are automatically distributed to the field (via Internet)*
 - *Updates are transparent to the user*

Feature Explanations

- Target power status LED
 - *Simple diagnostic reduces target connection problems*
 - *Comprehensive indicator of successful enumeration and cable update status*
- CE and FCC compliant
 - *No interference with test instrumentation and/or co-located equipment*
- Lead free roadmap
 - *Available under separate order number for compliance with EU RoHS regulations by July 2006*
- Compatible with target device Vcc in the range of +1.5 VDC to +3.3 VDC
 - *Extended product life as interface voltage specifications change*
- iMPACT and ChipScope support in ISE 6.3.03i (Windows only)
 - *Multiple applications use the same cable*
 - *Common API*
- Linux support in ISE 7.1i
 - *This will be the only Xilinx USB cable supported by ISE*
 - *MultiLINX software support will be eliminated by Sep, 2005*

Feature Explanations

- Supports Boundary Scan and Slave-serial configuration modes
 - *Can be used to emulate ISP PROMs during FPGA development and debug phase*
- Programs / configures all Xilinx devices
 - *IEEE 1149.1 and IEEE 1532 compliant*
 - *Timing margins tested and guaranteed*