

		XA9500XL Family			
		Part Number	XA9536XL	XA9572XL	XA95144XL
Logic Resources	System Gates	800	1,600	3,200	
	Macrocells	36	72	144	
	Product terms per Macrocell	90	90	90	
Clock Resources	Global Clocks	3	3	3	
	Product Term Clocks per Function Block	18	18	18	
I/O Resources	Maximum I/O	34	72	117	
	Input Voltage Compatible (V)	2.5/3.3/5	2.5/3.3/5	2.5/3.3/5	
	Output Voltage Compatible (V)	2.5/3.3	2.5/3.3	2.5/3.3	
Speed Grades	Min. pin-to-pin Logic Delay (ns)	15.5	15.5	15.5	
	Automotive I Speed Grades	-15	-15	-15	
	Automotive Q Speed Grades	-15	-15	-15	
Miscellaneous	Temperature Grades <sup>(1)</sup>	I, Q	I, Q	I	
	RoHS (Pb-free)	Yes	Yes	Yes	
	XA Released	Yes	Yes	Yes	

Package	Area <sup>(2)</sup>	Maximum User I/Os		
VQFP Packages (VQ): very thin QFP (VQG44: 0.8 mm lead spacing, VQG64 and VQG100: 0.5 mm lead spacing)				
VQG44	12 x 12 mm	34	34	
VQG64	12 x 12 mm		52	
VQG100	16 x 16 mm			
TQFP Packages (TQ): thin QFP (0.5 mm lead spacing)				
TQG100	16 x 16 mm		72	
TQG144	22 x 22 mm			
Chip Scale Packages (CP): wire-bond chip-scale BGA (0.5 mm ball spacing)				
CPG132	8 x 8 mm			
Chip Scale Packages (CS): wire-bond chip-scale BGA (0.8 mm ball spacing)				
CSG144	12 x 12 mm			117

Notes: 1. Temp Grade XA CPLD Automotive I (T<sub>a</sub> = -40°C to +85°C); Automotive Q (T<sub>a</sub> = -40°C to +105°C with T<sub>j</sub> maximum = +125°C).  
2. Area dimensions for lead-frame products are inclusive of the leads.