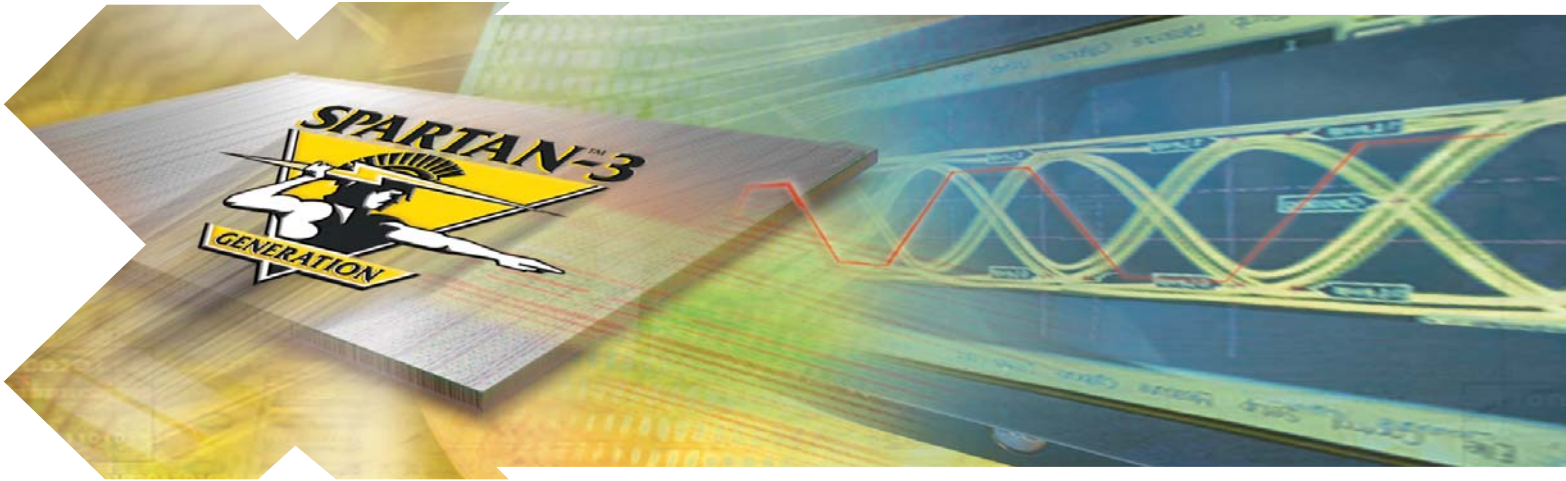




Xilinx Spartan™-3 Generation — Low Cost Memory Interfaces Made Easy



The Challenges of Building Low-Cost Memory Interfaces

- Building complex memory controller state machines and interfaces on a low-cost FPGA platform
- Capturing read data reliably under changing system conditions
- Completing and verifying the design faster with easy-to-use tools

The Xilinx Spartan-3 Generation FPGA Solution

- The Spartan™-3 Generation platform features an extensive offering of low-cost FPGAs with IO and gate-centric devices that minimize cost without compromising performance
- Comprehensive design resources including: hardware-verified reference designs, low-cost development boards, and the Memory Interface Generator (MIG) software help you design faster

Spartan-3 Generation FPGAs help you achieve your memory interface low cost design in the shortest possible time. With innovative reference designs and complete design tools Xilinx offers you the best solution in the industry.

Reliable Read Data Capture with LUT based calibration

Spartan-3 Generation implementations use a LUT-based adaptive delay circuit that ensures aligning the clock-to-the-data-valid-window, even under changing system conditions for reliable read data capture.

Low cost implementations with optimal FPGA devices

The extensive offerings of the Spartan-3 Generation FPGAs provide the optimal choice for IO or gate-centric implementations.

Hardware development made faster with low-cost boards

Starter kits and development boards with memory interface reference designs and demos help you drastically reduce your own design and verification time.

Complete Solutions with Easy-to-Use Software

The Memory Interface Generator (MIG) software, through a user-friendly GUI, helps you create your own interface, including HDL code and pin placement.

Comprehensive Memory Interface Design Resources

Xilinx offers all the design resources to make designing memory interfaces easier and faster.

Free Reference Designs and Detailed Applications Notes

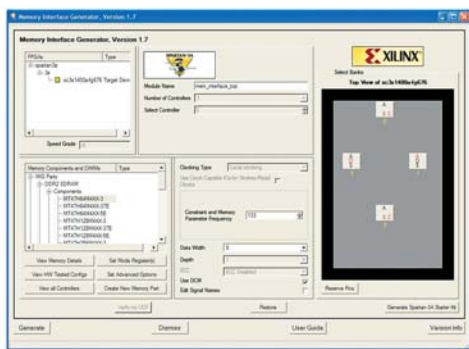
- Complete solutions are offered for popular memory interfaces

Memory Interface Support for Spartan-3 Generation FPGAs

Memory Architecture	Clock Rate	Data Rate	Spartan-3 Generation
DDR2 SDRAM	167 MHz	333 Mbps	Spartan-3, 3A, 3AN
DDR SDRAM	167 MHz	333 Mbps	Spartan-3, 3E, 3A, 3AN

Memory Interface Generator (MIG) Software Tool

- Easy to use menu-driven tool, accessed via CORE Generator™ software suite, helps you customize the memory interface reference design for your own requirements



Spartan-3 Generation Starter Kits and Development Boards

Xilinx Spartan-3 Generation reference designs are hardware verified on starter kits and development boards.



Development Boards

Spartan-3 Generation	Memory Architecture
Spartan-3	DDR SDRAM
Spartan-3E	DDR SDRAM
Spartan-3A	DDR2 SDRAM
Spartan-3AN	DDR2 SDRAM

Take The Next Step

For the latest information or to find out more about the complete Spartan-3 Generation FPGA memory interface solutions, visit: www.xilinx.com/memory

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