



Class Schedule by State

Important Notice:

1. Class Schedule list below is as of **November 10, 2009**.
2. Class dates are subject to change due to low enrollment. Please contact your local training representative if you have any questions.
3. After clicking on a list to register for a class offered by Xilinx, you will be required to login to our training catalog to checkout. If you have already created your account, please login with your username and password. If you have not created your account, please click the link "New User?" then proceed

If you have any questions, please contact the Registrar at (877) XLX-CLASS or registrar@xilinx.com

AL

Designing with VHDL

descr000000000105158

Date	Class#	Location	Facility	Price (USD)	TC	
11/25/09 - 11/27/09	00061687	AL		1800	18	Click here to Login & Register

ALBERTA

No class scheduled in this location in the next 4 months

AR

No class scheduled in this location in the next 4 months

AZ

[For this location, please click here to visit our ATP site for the complete class schedule](#)

BRITISH COLUMBIA

No class scheduled in this location in the next 4 months

CA - NORTHERN

Advanced FPGA Implementation

descr000000000105030

Date	Class#	Location	Facility	Price (USD)	TC	
12/15/09 - 12/16/09	00062953	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	Click here to Login & Register

Designing for Performance

descr000000000105031

Date	Class#	Location	Facility	Price (USD)	TC	
12/2/09 - 12/3/09	00062949	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	Click here to Login & Register
2/10/10 - 2/11/10	00063860	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	Click here to Login & Register

Designing with Multi-Gigabit Serial I/O

descr000000000104348

Date	Class#	Location	Facility	Price (USD)	TC	
11/17/09 - 11/19/09	00062947	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	Click here to Login & Register

Designing with the PlanAhead Analysis and Design Tool

descr000000000105107

Date	Class#	Location	Facility	Price (USD)	TC	
12/15/09 - 12/16/09	00062955	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	Click here to Login & Register

Designing with the Virtex-6 Family

descr000000000105169

Date	Class#	Location	Facility	Price (USD)	TC	
12/1/09 - 12/2/09	00063942	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	Click here to Login & Register

Designing with VHDL

descr000000000105080

Date	Class#	Location	Facility	Price (USD)	TC	
12/8/09 - 12/10/09	00062952	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1500	15	Click here to Login & Register

Embedded Open-Source Linux Development

descr000000000105106

Date	Class#	Location	Facility	Price (USD)	TC	
11/11/09 - 11/12/09	00063941	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1200	12	Click here to Login & Register

Essentials of FPGA Design

descr000000000105010

Date	Class#	Location	Facility	Price (USD)	TC	
12/1/09 - 12/1/09	00062948	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	500	5	Click here to Login & Register
2/9/10 - 2/9/10	00063858	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	500	5	Click here to Login & Register
3/2/10 - 3/2/10	00063861	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	500	5	Click here to Login & Register

CA - SOUTHERN**Designing for Performance**

descr000000000105031

Date	Class#	Location	Facility	Price (USD)	TC	
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Designing with the Spartan-6 and Virtex-6 Families

descr000000000124881

Date	Class#	Location	Facility	Price (USD)	TC	
1/26/10 - 1/28/10	00063842	CA - SOUTHERN	Holiday Inn - Costa Mesa Orange Co. Airport	1800	18	Click here to Login & Register

Essentials of FPGA Design

descr000000000105010

Date	Class#	Location	Facility	Price (USD)	TC	
12/14/09 - 12/14/09	00063226	CA - SOUTHERN	Holiday Inn - Costa Mesa Orange Co. Airport	600	6	Click here to Login & Register

[For this location, please click here to visit our ATP site for the complete class schedule.](#)**CO**

No class scheduled in this location in the next 4 months

CT

No class scheduled in this location in the next 4 months

DC

No class scheduled in this location in the next 4 months

DE

No class scheduled in this location in the next 4 months

FL**Advanced FPGA Implementation**

descr000000000105030

Date	Class#	Location	Facility	Price (USD)	TC	
1/28/10 - 1/29/10	00063671	FL		1400	14	Click here to Login & Register

Designing with the Spartan-6 and Virtex-6 Families

descr000000000105140

Date	Class#	Location	Facility	Price (USD)	TC	
1/25/10 - 1/27/10	00063667	FL		1800	18	Click here to Login & Register

Embedded Systems Development

descr000000000105048

Date	Class#	Location	Facility	Price (USD)	TC	
12/2/09 - 12/3/09	00063649	FL		1400	14	Click here to Login & Register

Designing with the PlanAhead Analysis and Design Tool

descr000000000105107

Date	Class#	Location	Facility	Price (USD)	TC
1/6/10 - 1/7/10	00063657	GA	Hyatt Place Atlanta/Duluth/Johns Creek	1400	14

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Designing with the Spartan-6 and Virtex-6 Families

descr000000000105140

Date	Class#	Location	Facility	Price (USD)	TC
2/23/10 - 2/25/10	00063668	GA	Hyatt Place Atlanta/Duluth/Johns Creek	1800	18

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Designing with VHDL

descr000000000105080

Date	Class#	Location	Facility	Price (USD)	TC
12/8/09 - 12/10/09	00063613	GA	Hyatt Place Atlanta/Duluth/Johns Creek	1800	18

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Embedded Systems Development

descr000000000105048

Date	Class#	Location	Facility	Price (USD)	TC
2/16/10 - 2/17/10	00063652	GA	Hyatt Place Atlanta/Duluth/Johns Creek	1400	14

[Click here to Login & Register](#)

No class scheduled in this location in the next 4 months

No class scheduled in this location in the next 4 months

Advanced VHDL

descr000000000105165

Date	Class#	Location	Facility	Price (USD)	TC
3/4/10 - 3/5/10	00063936	IL		1200	12

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Designing for Performance

descr000000000105031

Date	Class#	Location	Facility	Price (USD)	TC
2/23/10 - 2/24/10	00063930	IL		1200	12
2/23/10 - 2/24/10	00063929	IL		1200	12

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[Click here to Login & Register](#)

Designing with Verilog

descr000000000105078

Date	Class#	Location	Facility	Price (USD)	TC	
3/8/10 - 3/10/10	00063934	IL		1800	18	Click here to Login & Register

Designing with VHDL

descr000000000105080

Date	Class#	Location	Facility	Price (USD)	TC	
3/1/10 - 3/3/10	00063935	IL		1800	18	Click here to Login & Register

DSP Design Using System Generator

descr000000000104861

Date	Class#	Location	Facility	Price (USD)	TC	
2/25/10 - 2/26/10	00063937	IL		1200	12	Click here to Login & Register

Essentials of FPGA Design

descr000000000124999

Date	Class#	Location	Facility	Price (USD)	TC	
2/22/10 - 2/22/10	00063927	IL		600	6	Click here to Login & Register

IN

No class scheduled in this location in the next 4 months

Internet (e-Learning)

No class scheduled in this location in the next 4 months

KS

No class scheduled in this location in the next 4 months

KY

No class scheduled in this location in the next 4 months

LA

No class scheduled in this location in the next 4 months

MA

Designing for Performance

descr000000000105031

Date	Class#	Location	Facility	Price (USD)	TC	
12/16/09 - 12/17/09	00063610	MA	Genesis Associates - Burlington, MA	1300	13	Click here to Login & Register
12/16/09 - 12/17/09	00061724	MA	Genesis Associates - Burlington, MA	1300	13	Click here to Login & Register

Designing with Ethernet MAC Controllers

descr000000000104342

Date	Class#	Location	Facility	Price (USD)	TC	
3/9/10 - 3/10/10	00063684	MA	Genesis Associates - Burlington, MA	1600	16	Click here to Login & Register
3/9/10 - 3/10/10	00063684	MA	Genesis Associates - Burlington, MA	1600	16	Click here to Login & Register

Designing with the PlanAhead Analysis and Design Tool

descr000000000105107

Date	Class#	Location	Facility	Price (USD)	TC	
2/10/10 - 2/11/10	00063658	MA	Genesis Associates - Burlington, MA	1400	14	Click here to Login & Register

Designing with the Virtex-6 Family

descr000000000105169

Date	Class#	Location	Facility	Price (USD)	TC	
11/17/09 - 11/19/09	00063485	MA	Genesis Associates - Burlington, MA	1800	18	Click here to Login & Register

DSP Design Using System Generator

descr000000000104861

Date	Class#	Location	Facility	Price (USD)	TC	
1/13/10 - 1/14/10	00063663	MA	Genesis Associates - Burlington, MA	1400	14	Click here to Login & Register

Essentials of FPGA Design

descr000000000105010

Date	Class#	Location	Facility	Price (USD)	TC	
12/15/09 - 12/15/09	00063607	MA	Genesis Associates - Burlington, MA	600	6	Click here to Login & Register

Fundamentals of FPGA Design

descr000000000104856

Date	Class#	Location	Facility	Price (USD)	TC	
12/15/09 - 12/15/09	00061716	MA	Genesis Associates - Burlington, MA	600	6	Click here to Login & Register
12/15/09 - 12/15/09	00061716	MA	Genesis Associates - Burlington, MA	600	6	Click here to Login & Register

[For this location, please click here to visit our ATP site for the complete class schedule](#)**MANITOBA**

No class scheduled in this location in the next 4 months

MD**Designing with Multi-Gigabit Serial I/O**

descr000000000104348

Date	Class#	Location	Facility	Price (USD)	TC	
12/10/09 - 12/11/09	00063005	MD	Genesis Mid-Atlantic - Columbia, MD - USA (14)	1400	14	Click here to Login & Register
12/10/09 - 12/11/09	00063005	MD	Genesis Mid-Atlantic - Columbia, MD - USA (14)	1400	14	Click here to Login & Register

Designing with the Spartan-6 and Virtex-6 Families

descr000000000105140

Date	Class#	Location	Facility	Price (USD)	TC	
11/12/09 - 11/13/09	00063003	MD	Genesis Mid-Atlantic - Columbia, MD - USA (14)	1400	14	Click here to Login & Register

ME

No class scheduled in this location in the next 4 months

MI

Advanced Features and Techniques of Embedded Systems Development

descr000000000104867

Date	Class#	Location	Facility	Price (USD)	TC	
11/18/09 - 11/19/09	00063960	MI		1200	12	Click here to Login & Register

MN

Advanced VHDL

descr000000000105165

Date	Class#	Location	Facility	Price (USD)	TC	
2/8/10 - 2/9/10	00063925	MN		1000	10	Click here to Login & Register

Designing for Performance

descr000000000105031

Date	Class#	Location	Facility	Price (USD)	TC	
1/28/10 - 1/29/10	00063919	MN		1200	12	Click here to Login & Register

Designing with the Spartan-6 and Virtex-6 Families

descr000000000105140

Date	Class#	Location	Facility	Price (USD)	TC	
2/10/10 - 2/12/10	00063921	MN		1800	18	Click here to Login & Register

Designing with Verilog

descr000000000105078

Date	Class#	Location	Facility	Price (USD)	TC	
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Designing with VHDL

descr000000000105080

Date	Class#	Location	Facility	Price (USD)	TC
2/1/10 - 2/3/10	00063924	MN		1800	18

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descr000000000104861

Date	Class#	Location	Facility	Price (USD)	TC
1/14/10 - 1/15/10	00063926	MN		1200	12

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descr000000000105048

Date	Class#	Location	Facility	Price (USD)	TC
1/25/10 - 1/26/10	00063920	MN		1200	12

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descr000000000105125

Date	Class#	Location	Facility	Price (USD)	TC
1/18/10 - 1/19/10	00063922	MN		1200	12

[Click here to Login & Register](#)**Essentials of FPGA Design**

descr000000000124999

Date	Class#	Location	Facility	Price (USD)	TC
1/27/10 - 1/27/10	00063914	MN		600	6

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descr000000000105165

Date	Class#	Location	Facility	Price (USD)	TC
12/3/09 - 12/4/09	00062794	MO		1200	12

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descr000000000105140

Date	Class#	Location	Facility	Price (USD)	TC
12/8/09 - 12/10/09	00062795	MO		1800	18

[Click here to Login & Register](#)**Designing with Verilog**

descr000000000105078

Date	Class#	Location	Facility	Price (USD)	TC
11/18/09 - 11/20/09	00062792	MO		1800	18

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Designing with VHDL

descr000000000105080

Date	Class#	Location	Facility	Price (USD)	TC
11/30/09 - 12/2/09	00062793	MO		1800	18

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Embedded Systems Development

descr000000000105048

Date	Class#	Location	Facility	Price (USD)	TC
11/12/09 - 11/13/09	00062790	MO		1200	12

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Embedded Systems Software Development

descr000000000105125

Date	Class#	Location	Facility	Price (USD)	TC
11/16/09 - 11/17/09	00062791	MO		1200	12

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NC

Designing with the PlanAhead Analysis and Design Tool

descr000000000105107

Date	Class#	Location	Facility	Price (USD)	TC
3/4/10 - 3/5/10	00063659	NC		1400	14

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Designing with VHDL

descr000000000105080

Date	Class#	Location	Facility	Price (USD)	TC
3/1/10 - 3/3/10	00063677	NC		1800	18

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DSP Implementation Techniques for Xilinx FPGAs - DO NOT USE

descr000000000104366

Date	Class#	Location	Facility	Price (USD)	TC
1/13/10 - 1/15/10	00063665	NC		2100	21
1/13/10 - 1/15/10	00063665	NC		2100	21

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[Click here to Login & Register](#)

ND

No class scheduled in this location in the next 4 months

NE

No class scheduled in this location in the next 4 months

NH

[For this location, please click here to visit our ATP site for the complete class schedule](#)

NJ

Advanced Features and Techniques of Embedded Systems Development

descr000000000104867

Date	Class#	Location	Facility	Price (USD)	TC	
12/3/09 - 12/4/09	00063011	NJ		1400	14	Click here to Login & Register

Designing with the PlanAhead Analysis and Design Tool

descr000000000105107

Date	Class#	Location	Facility	Price (USD)	TC	
11/12/09 - 11/13/09	00063009	NJ		1400	14	Click here to Login & Register

Embedded Open-Source Linux Development

descr000000000105106

Date	Class#	Location	Facility	Price (USD)	TC	
12/10/09 - 12/11/09	00063268	NJ		1400	14	Click here to Login & Register

Embedded Systems Development

descr000000000105048

Date	Class#	Location	Facility	Price (USD)	TC	
12/1/09 - 12/2/09	00063010	NJ		1400	14	Click here to Login & Register

Embedded Systems Software Development

descr000000000105125

Date	Class#	Location	Facility	Price (USD)	TC	
12/8/09 - 12/9/09	00063267	NJ		1400	14	Click here to Login & Register

NM

[For this location, please click here to visit our ATP site for the complete class schedule](#)

NORTEL

No class scheduled in this location in the next 4 months

NV

[For this location, please click here to visit our ATP site for the complete class schedule](#)

NY

Designing for Performance

descr000000000105031

Date	Class#	Location	Facility	Price (USD)	TC	
11/19/09 - 11/20/09	00063014	NY	Avnet - Rochester, NY	1400	14	Click here to Login & Register

OH

No class scheduled in this location in the next 4 months

OK

No class scheduled in this location in the next 4 months

ONTARIO

Advanced FPGA Implementation

descr000000000105030

Date	Class#	Location	Facility	Price (USD)	TC	
1/21/10 - 1/22/10	00063672	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1400	14	Click here to Login & Register

Advanced VHDL

descr000000000104464

Date	Class#	Location	Facility	Price (USD)	TC	
11/16/09 - 11/17/09	00061719	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1200	12	Click here to Login & Register
2/22/10 - 2/23/10	00063681	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1200	12	Click here to Login & Register
2/22/10 - 2/23/10	00063682	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1200	12	Click here to Login & Register

Designing for Performance

descr000000000104564

Date	Class#	Location	Facility	Price (USD)	TC	
11/19/09 - 11/20/09	00061723	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1300	13	Click here to Login & Register
11/19/09 - 11/20/09	00061723	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1300	13	Click here to Login & Register

Designing with the PlanAhead Analysis and Design Tool

descr000000000105107

Date	Class#	Location	Facility	Price (USD)	TC	
12/3/09 - 12/4/09	00063656	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1400	14	Click here to Login & Register

Designing with the Virtex-6 Family

descr000000000105169

Date	Class#	Location	Facility	Price (USD)	TC	
12/15/09 - 12/17/09	00063486	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1800	18	Click here to Login & Register

Designing with VHDL

descr000000000105080

Date	Class#	Location	Facility	Price (USD)	TC	
2/2/10 - 2/4/10	00063676	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1800	18	Click here to Login & Register

Embedded Systems Development

descr000000000105048

Date	Class#	Location	Facility	Price (USD)	TC	
1/19/10 - 1/20/10	00063650	ONTARIO	Electro-Source - Rexdale (Toronto), CANADA	1400	14	Click here to Login & Register

Essentials of FPGA Design

descr000000000105010

Date	Class#	Location	Facility	Price (USD)	TC	
11/18/09 - 11/18/09	00063606	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	600	6	Click here to Login & Register

Fundamentals of FPGA Design

descr000000000104856

Date	Class#	Location	Facility	Price (USD)	TC	
11/18/09 - 11/18/09	00061715	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	600	6	Click here to Login & Register
11/18/09 - 11/18/09	00061715	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	600	6	Click here to Login & Register

Minimizing Your Design Time with the ChipScope Pro Debug and Verification Tools

descr000000000104393

Date	Class#	Location	Facility	Price (USD)	TC	
2/5/10 - 2/5/10	00063678	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	700	7	Click here to Login & Register
2/5/10 - 2/5/10	00063678	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	700	7	Click here to Login & Register

OR

No class scheduled in this location in the next 4 months

PA - EASTERN

No class scheduled in this location in the next 4 months

PA - WESTERN

No class scheduled in this location in the next 4 months

QUEBEC

Advanced VHDL

descr000000000104464

Date	Class#	Location	Facility	Price (USD)	TC	
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Embedded Systems Development

descr000000000105048

Date	Class#	Location	Facility	Price (USD)	TC
1/21/10 - 1/22/10	00063651	QUEBEC	Hardent - Montreal, Quebec, CAN	1400	14

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No class scheduled in this location in the next 4 months

SD

No class scheduled in this location in the next 4 months

TN

No class scheduled in this location in the next 4 months

TX**Advanced Features and Techniques of Embedded Systems Development**

descr000000000104867

Date	Class#	Location	Facility	Price (USD)	TC
11/18/09 - 11/19/09	00063870	TX	Xilinx Learning Center - Dallas, TX , USA (20)	1200	12
1/28/10 - 1/29/10	00063287	TX	Xilinx Learning Center - Dallas, TX , USA (20)	1200	12

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descr000000000105030

Date	Class#	Location	Facility	Price (USD)	TC
2/25/10 - 2/26/10	00063311	TX	Xilinx Learning Center - Dallas, TX , USA (20)	1200	60

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descr000000000105165

Date	Class#	Location	Facility	Price (USD)	TC
3/9/10 - 3/10/10	00063313	TX	Xilinx Learning Center - Dallas, TX , USA (20)	1200	12

[Click here to Login & Register](#)**Debugging Techniques Using the ChipScope Pro Tools**

descr000000000104863

Date	Class#	Location	Facility	Price (USD)	TC
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Designing a LogiCORE PCI Express System

descr000000000105026

Date	Class#	Location	Facility	Price (USD)	TC	
2/4/10 - 2/5/10	00063306	TX	Xilinx Learning Center - Dallas, TX , USA (20)	1200	12	Click here to Login & Register

Designing for Performance

descr000000000105031

Date	Class#	Location	Facility	Price (USD)	TC	
2/11/10 - 2/12/10	00063308	TX	Xilinx Learning Center - Dallas, TX , USA (20)	1200	60	Click here to Login & Register

Designing with Multi-Gigabit Serial I/O

descr000000000125003

Date	Class#	Location	Facility	Price (USD)	TC	
12/1/09 - 12/3/09	00063871	TX	Xilinx Learning Center - Dallas, TX , USA (20)	1800	18	Click here to Login & Register
3/3/10 - 3/5/10	00063312	TX	Xilinx Learning Center - Dallas, TX , USA (20)	1800	18	Click here to Login & Register
3/3/10 - 3/5/10	00063312	TX	Xilinx Learning Center - Dallas, TX , USA (20)	1800	18	Click here to Login & Register

Designing with the PlanAhead Analysis and Design Tool

descr000000000105107

Date	Class#	Location	Facility	Price (USD)	TC	
1/21/10 - 1/22/10	00063286	TX	Xilinx Learning Center - Dallas, TX , USA (20)	1200	60	Click here to Login & Register

Designing with the Virtex-5 Family

descr000000000104982

Date	Class#	Location	Facility	Price (USD)	TC	
2/24/10 - 2/24/10	00063310	TX	Xilinx Learning Center - Dallas, TX , USA (20)	600	6	Click here to Login & Register
2/24/10 - 2/24/10	00063310	TX	Xilinx Learning Center - Dallas, TX , USA (20)	600	6	Click here to Login & Register

DSP Design Using System Generator

descr000000000104861

Date	Class#	Location	Facility	Price (USD)	TC	
2/18/10 - 2/19/10	00063309	TX	Xilinx Learning Center - Dallas, TX , USA (20)	1200	12	Click here to Login & Register

Embedded Systems Development

descr000000000105048

Date	Class#	Location	Facility	Price (USD)	TC	
11/16/09 - 11/17/09	00063869	TX	Xilinx Learning Center - Dallas, TX , USA (20)	1200	12	Click here to Login & Register

UT**Designing with the Spartan-6 and Virtex-6 Families**

descr000000000105140

Date	Class#	Location	Facility	Price (USD)	TC	
12/8/09 - 12/10/09	00063367	UT	Guru Labs - Bountiful - UT	1500	15	Click here to Login & Register

Signal Integrity and Board Design for Xilinx FPGAs

descr000000000104988

Date	Class#	Location	Facility	Price (USD)	TC	
1/12/10 - 1/14/10	00061986	UT		1200	12	Click here to Login & Register

VA

No class scheduled in this location in the next 4 months

VT[For this location, please click here to visit our ATP site for the complete class schedule.](#)**WA**

No class scheduled in this location in the next 4 months

WI

No class scheduled in this location in the next 4 months