

Call for Papers & Demonstrations

1st Asia NetFPGA Developers Workshop

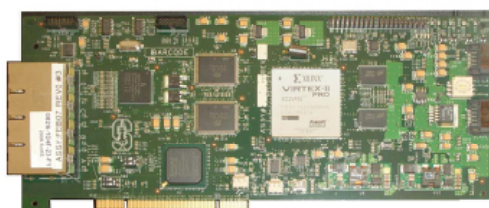
13-15 June 2010 at KAIST, Daejeon, Korea

Full Details: <http://fif.kr/AsiaNetFPGAs>,
<http://netfpga.org/php/events.php>

NetFPGA Enthusiasts,

You already know that the NetFPGA implements a Gigabit NIC, a hardware-accelerated Internet router, a traffic generator, an OpenFlow switch, a NetFlow probe, and more. What else can it do? We invite you, NetFPGA Developers, to join the 1st NetFPGA Developers Workshop in Asia. The workshop to be held at KAIST (Korea Advanced Institute of Science and Technology) will allow you to show the world your latest project. Submit a paper to describe your project, prepare a demo, and come to Daejeon Metropolitan City of Korea in June of 2010 to demonstrate your work!

- **Paper Submission Deadline: 28 February 2010**
- **Paper Format: 4 ~ 8 Pages, ACM-style**
- **Demonstrations: Run on NetFPGA(s)**



“What have you built with your NetFPGA Hardware?”

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