Design Services Partner Profile

AMD XILINX

eVS embedded Vision Systems S.r.L. Company Overview

Partner Details

Partner Tier:	Select Certified
Headquarters Location:	Strada Le Grazie 15,Verona,,Italy
Xilinx Alliance Partner Since:	11/2/2009

Key Contact Info

Roberto Marzotto
roberto.marzotto@evsys.it

Regions Served

APAC, Western Europe

Supported Product Types

Yes	Design Services	No	Embedded Products
Yes	IP	No	EDA Tools
No	Boards		

Engineering Service Types

Yes	General Purpose Services	
Yes	Services in Support of Products Sold	Preferred
No	Services Leading to Contract Manufacturing	
Maximum Service Scope:	Chip level FPGA / ASIC design	

Engagement Models Supported

Yes	SOW-Based Fixed Bid Projects	Preferred
Yes	Time and Materials	
At my location	Captive Resources at Partner or Customer Premises	

Xilinx Design Tool Experience

Yes	Vivado	No	SDSoC
Yes	HLx	No	SDAccel

Industry Certifications Achieved

Industry Certification Services Offered

Government Clearances

None

eVS embedded Vision Systems S.r.L. Segment and Application Focus

No Focus	**Defense/Aerospace**	No Focus	Homeland Security
		No Focus	Electronic Warfare
		No Focus	ISR
		No Focus	Space
		No Focus	Missiles & Munitions
		No Focus	Military Avionics
		No Focus	Commercial Avionics
		No Focus	SatComm
		No Focus	Wireless Milcom
		No Focus	Other A&D

No Focus	**Audio, Video, Broadcast**	No Focus	Encoders & Decoders
		No Focus	Monitors & Projection
		No Focus	Networking & Converters
		No Focus	Audio Systems
		No Focus	Video Processing Cards
		No Focus	Video Conferencing
		No Focus	Routers and Switches
		No Focus	Pro Cameras & Camcorders
		No Focus	Other Broadcast

Primary Focus	**Automotive & Transport**	Primary Focus	Driver Assist - Camera
		No Focus	Driver Assist Radar/LIDAR
		No Focus	Driver Info/Infotainment
		No Focus	Automated Driving
		No Focus	Other Automotive

No Focus	**Consumer**	No Focus	Digital Display
		No Focus	Drones
		No Focus	Multifunction Printers
		No Focus	Other Consumer

No Focus	**Datacenter**	No Focus	Storage
		No Focus	Compute
		No Focus	Networking

Primary Focus	**Industrial, Medical**	No Focus	Motor Control
		No Focus	Industrial Networking
		No Focus	Surveillance
		No Focus	Machine Vision
		No Focus	Medical Imaging
		No Focus	Medical / Clinical
		No Focus	Other ISM

Design Services Partner Profile

AMD XILINX

eVS embedded Vision Systems S.r.L. Segment and Application Focus (continued)

No Focus	**Test & Measurement**	No Focus	Simulation / Emulation
		No Focus	T & M Instrumentation
		No Focus	Semiconductor Test
		No Focus	Wired Communication Test
		No Focus	Wireless Communications Test
		No Focus	Other Test & Measurement

No Focus	**Wired Comms & Networks**	No Focus	Access Equipment
		No Focus	Long Haul/Transport
		No Focus	Wired Mobile Backhaul
		No Focus	OTN
		No Focus	Enterprise Equipment
		No Focus	Ethernet Interlaken Connectivity
		No Focus	Packet Processing
		No Focus	Other Wired Communications

No Focus	**Wireless Comms & Networks**	No Focus	Baseband
		No Focus	Wireless Backhaul
		No Focus	Connectivity & Switching
		No Focus	Radio
		No Focus	Other Wireless

Design Services Partner Profile

Design Competencies

System Architecture	No Focus	
RF and Analog Design	No Focus	
FPGA Design and Integration	Primary Focus	hw-sw co-design and optimization of image processing, computer vision and machine learning algorithms in FPGA/SoC. Optimal PL/PS partitioning. Design verification, code and functional coverage.
FPGA Timing Closure	Secondary Focus	
FPGA Design Optimization	Secondary Focus	NPU and inference engine customization and optimization
Signal Integrity Design and Analysis	No Focus	
Inhouse or Subcontracted:		
Typical Serial Speed:	0	
PCB Design & Layout	No Focus	
Inhouse or Subcontracted:		
Industrial and Mechanical Design	No Focus	
Inhouse or Subcontracted:		
Industry Certification and Compliance Testing	No Focus	
Inhouse or Subcontracted:		
Quality and Reliability Testing	No Focus	
Inhouse or Subcontracted:		
Contract Manufacturing	No Focus	
Inhouse or Subcontracted:		
Max Quantities Supported:		
Digital Signal Processing	Secondary Focus	Long experience with System Generator for DSP
Experience:		
Embedded Processors	Secondary Focus	
Experience:	Zynq, MicroBlaze, Atom, Cortex A9, MIPS	
Embedded Porting /	Secondary Focus	

Design Services Partner Profile

AMD XILINX

BSP Extension

OS and RTOS Design Experience:	Linux

Embedded Application	Secondary Focus	Examples: Multi-camera system for boat docking. Bird detection system for airports. Vision system for textile label machines. Vision system for 3D inspection of glass bottle forming machines. People counting.
Development		

Image Sensor Fusion

Homo or Heterogeneous:	
Sensor Types Design Experience:	

Computer Vision	Primary Focus	Stereo vision, image warping, segmentation, model tracking, 3D reconstruction, optical flow, slam, pattern matching, calibration, feature detection, mosaicking.

Machine Learning	Primary Focus	Detection, Classification, Segmentation, Deep Learning. Domain Adaptation, Semi-supervised/Multimodal/Incremental Learning, Distillation. NN optimization/quantization/compression. Traditional techniques (e.g. SVM)
Framework Experience:	TensorFlow	
Network Model Experience:	AlexNet, FCN, Other / Contact Partner, ResNet, VGG	