

Design Services Partner Profile



BLT Company Overview

Date : 06/19/2023

www.blinc.com

Partner Details

Partner Tier: Premier
Headquarters Location: 9881 Broken Land Pkwy, Columbia, MD, USA
Xilinx Alliance Partner Since: 9/1/2010

Key Contact Info

Ed McCauley
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Regions Served

North America Central, North America East, North America West

Supported Product Types

Yes	Design Services	No	Embedded Products
No	IP	No	EDA Tools
No	Boards		

Engineering Service Types

Yes	General Purpose Services
Yes	Services in Support of Products Sold
Yes	Services Leading to Contract Manufacturing
Maximum Service Scope:	Turnkey / full product design

Engagement Models Supported

Yes	SOW-Based Fixed Bid Projects
Yes	Time and Materials
Both	Captive Resources at Partner or Customer Premises

Xilinx Design Tool Experience

Yes	Vivado	Yes	SDSoC
Yes	HLx	Yes	SDAccel

Industry Certifications Achieved

Other / Contact Partner

Industry Certification Services Offered

Government Clearances

Top Secret/SCI, ITAR

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BLT Segment and Application Focus

Primary Focus	<u>Defense/Aerospace</u>	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Homeland Security Electronic Warfare ISR Space Missiles & Munitions Military Avionics Commercial Avionics SatComm Wireless Milcom Other A&D
Secondary Focus	<u>Audio, Video, Broadcast</u>	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Encoders & Decoders Monitors & Projection Networking & Converters Audio Systems Video Processing Cards Video Conferencing Routers and Switches Pro Cameras & Camcorders Other Broadcast
No Focus	<u>Automotive & Transport</u>	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Driver Assist - Camera Driver Assist Radar/LIDAR Driver Info/Infotainment Automated Driving Other Automotive
Secondary Focus	<u>Consumer</u>	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Digital Display Drones Multifunction Printers Other Consumer
No Focus	<u>Datacenter</u>	<ul style="list-style-type: none"> No Focus No Focus No Focus 	<ul style="list-style-type: none"> Storage Compute Networking
Secondary Focus	<u>Industrial, Medical</u>	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Motor Control Industrial Networking Surveillance Machine Vision Medical Imaging Medical / Clinical Other ISM

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BLT Segment and Application Focus (continued)

Primary Focus	Test & Measurement	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Simulation / Emulation T & M Instrumentation Semiconductor Test Wired Communication Test Wireless Communications Test Other Test & Measurement
Primary Focus	Wired Comms & Networks	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Access Equipment Long Haul/Transport Wired Mobile Backhaul OTN Enterprise Equipment Ethernet Interlaken Connectivity Packet Processing Other Wired Communications
Primary Focus	Wireless Comms & Networks	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Baseband Wireless Backhaul Connectivity & Switching Radio Other Wireless

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Design Competencies

System Architecture	Primary Focus	BLT designs FPGAs, MPSoCs, ACAPs, embedded software, high-speed/high density PCBs, and complete systems.
RF and Analog Design	Secondary Focus	BLT handles up to 6 GHZ design work in-house. Above that, we engage trusted firms to meet our Clients' needs.
FPGA Design and Integration	Primary Focus	The founder of BLT helped negotiate Xilinx's original patents. BLT has been designing and pioneering the field of FPGAs from 1987 and continued to do so after spinning off from Xilinx in 1989.
FPGA Timing Closure	Primary Focus	Absolutely! FPGA/SoC cannot be reliably designed without performing Timing Closure. BLT has worked with Xilinx to develop and codify the Xilinx's UltraFast design flow.
FPGA Design Optimization	Primary Focus	Using BLT's design techniques and methodologies, our Clients' designs can fit into smaller, slower speed-grade devices thus saving thousands, often effectively paying for our services.
Signal Integrity Design and Analysis	Primary Focus	Absolutely! FPGA and MPSoC designs must take signal integrity into consideration. BLT's PCB designs also account for a multitude of SI facets. Example: 6U VPX card with FIFTY (50) 10G pairs out the rear.
	Inhouse or Subcontracted: Typical Serial Speed:	Both
PCB Design & Layout	Primary Focus	BLT designs custom PCBs for the majority of our Client engagements.
	Inhouse or Subcontracted:	Both
Industrial and Mechanical Design	Secondary Focus	BLT has mechanical engineers on staff.
	Inhouse or Subcontracted:	Both
Industry Certification and Compliance Testing	No Focus	
	Inhouse or Subcontracted:	
Quality and Reliability Testing	No Focus	
	Inhouse or Subcontracted:	
Contract Manufacturing	Secondary Focus	BLT partners with several CMs to provide world-class solutions. Many offer space-grade capabilities and are able to serve a variety of government requirements as well.
	Inhouse or Subcontracted:	Both
	Max Quantities Supported:	10,000+
Digital Signal Processing	Primary Focus	BLT has several (Hardware) DSP engineers on staff.
	Experience:	
Embedded Processors	Primary Focus	BLT was one of the first companies IN THE WORLD to receive Zynq(R) silicon. We created XIP (Execute in Place) technology for Xilinx and, as Xilinx ATPs, CREATED the training curriculum and contents for Xilinx SoCs.
	Experience:	



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Embedded Porting / BSP Extension OS and RTOS Design Experience:	Primary Focus	Absolutely! SoC/MPSoC design can't "talk" to the outside world (PCB) without hardware and driver support.
Embedded Application Development	Secondary Focus	BLT can develop embedded applications; however, we generally suggest that, as experts "in their world," Clients create their high-level applications, while BLT focuses on the FPGA/SoC/firmware/driver parts of designs.
Image Sensor Fusion Homo or Heterogeneous: Sensor Types Design Experience:	Secondary Focus Both	Experience with camera and high-speed ADC/DACs. VAST experience and an early pioneer with Xilinx's RFSoc.
Computer Vision	No Focus	
Machine Learning Framework Experience: Network Model Experience:	Secondary Focus	BLT has staff that works with TensorFlow
