

Design Services Partner Profile



Sital Technology and Hardware Engineering, Ltd. Company Overview

Date : 02/21/2023

www.sitaltech.com

Partner Details

Partner Tier: Select
Headquarters Location: Atir Yeda 5b,Kfar Saba,,Israel
Xilinx Alliance Partner Since: 5/29/2012

Key Contact Info

Robert McBride
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Regions Served

Supported Product Types

Yes	Design Services	Yes	Embedded Products
Yes	IP	No	EDA Tools
Yes	Boards		

Engineering Service Types

No	General Purpose Services		
Yes	Services in Support of Products Sold	Preferred	
No	Services Leading to Contract Manufacturing		
	Maximum Service Scope: Chip level FPGA / ASIC design		

Engagement Models Supported

No	SOW-Based Fixed Bid Projects		
Yes	Time and Materials	Preferred	
	Captive Resources at Partner or Customer Premises		

Xilinx Design Tool Experience

Yes	Vivado	No	SDSoC
Yes	HLx	No	SDAccel

Industry Certifications Achieved ISO 9001

Industry Certification Services Offered DO-178, DO-254, FCC, UL

Government Clearances Yes

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Sital Technology and Hardware Engineering, Ltd. Segment and Application Focus

Primary Focus	<u>Defense/Aerospace</u>	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Homeland Security Electronic Warfare ISR Space Missiles & Munitions Military Avionics Commercial Avionics SatComm Wireless Milcom Other A&D
No Focus	<u>Audio, Video, Broadcast</u>	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Encoders & Decoders Monitors & Projection Networking & Converters Audio Systems Video Processing Cards Video Conferencing Routers and Switches Pro Cameras & Camcorders Other Broadcast
Secondary Focus	<u>Automotive & Transport</u>	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Driver Assist - Camera Driver Assist Radar/LIDAR Driver Info/Infotainment Automated Driving Other Automotive
No Focus	<u>Consumer</u>	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Digital Display Drones Multifunction Printers Other Consumer
No Focus	<u>Datacenter</u>	<ul style="list-style-type: none"> No Focus No Focus No Focus 	<ul style="list-style-type: none"> Storage Compute Networking
No Focus	<u>Industrial, Medical</u>	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Motor Control Industrial Networking Surveillance Machine Vision Medical Imaging Medical / Clinical Other ISM

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Sital Technology and Hardware Engineering, Ltd. Segment and Application Focus (continued)

Secondary Focus	Test & Measurement	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Simulation / Emulation T & M Instrumentation Semiconductor Test Wired Communication Test Wireless Communications Test Other Test & Measurement
Secondary Focus	Wired Comms & Networks	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Access Equipment Long Haul/Transport Wired Mobile Backhaul OTN Enterprise Equipment Ethernet Interlaken Connectivity Packet Processing Other Wired Communications
No Focus	Wireless Comms & Networks	<ul style="list-style-type: none"> No Focus No Focus No Focus No Focus No Focus 	<ul style="list-style-type: none"> Baseband Wireless Backhaul Connectivity & Switching Radio Other Wireless

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Design Competencies

System Architecture	No Focus	
RF and Analog Design	No Focus	
FPGA Design and Integration	Primary Focus	Sital Technology develop serial communication IPs for Xilinx FPGAs. The IPs are supplied as fully HW + SW integrated.
FPGA Timing Closure	Secondary Focus	All of our IPs are supplied after successful gate level simulation and synthesis in VIVADO. Time closure is always met.
FPGA Design Optimization	Secondary Focus	Sital provides the IP in VHDL netlist that is technology independent, and it can be place and routed to any FPGA family. This provides future proof.
Signal Integrity Design and Analysis	Secondary Focus	Sital performs signal integrity on its boards to verify proper coupling for serial communication IPs to maintain the typical impedance.
	Inhouse or Subcontracted: Inhouse Typical Serial Speed: 10000000	
PCB Design & Layout	Secondary Focus	Develop IO and SBC cards in house.
	Inhouse or Subcontracted: Inhouse	
Industrial and Mechanical Design	No Focus	
	Inhouse or Subcontracted:	
Industry Certification and Compliance Testing	Primary Focus	Sital develop DO254 for our IPs in house with Guide of LogiCircuit and ConsuNova
	Inhouse or Subcontracted: Inhouse	
Quality and Reliability Testing	Primary Focus	Our products are targeted to Space and Avionics, and go through rigid testing before delivery.
	Inhouse or Subcontracted: Inhouse	
Contract Manufacturing	No Focus	
	Inhouse or Subcontracted: Max Quantities Supported:	
Digital Signal Processing	No Focus	
	Experience:	
Embedded Processors	Secondary Focus	Sital Technology SVIVADO is a complete solution which includes: Embedded processor + AXI + Sital IP + PMOD transceiver And the relevant SW: user example + API + BSP that runs on the hardware
	Experience: MPSOC MicroBlaze	
Embedded Porting / BSP Extension	Secondary Focus	SVIVADO is supplied with choice of RTOS.
	OS and RTOS Design Experience: FreeRTOS, Linux, QNX, VxWorks	

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**Embedded Application
Development**

Primary Focus Applications for SBC and IOs

Image Sensor Fusion

No Focus

Homo or Heterogeneous:

Sensor Types Design Experience:

Computer Vision

No Focus

Machine Learning

No Focus

Framework Experience:

Network Model Experience:
