

[View in Browser](#)



Developer News



Join the 2021 Adaptive Computing Challenge and enter to win up to \$10,000

[> Learn More](#)



Introducing Vitis™ Video Analytics SDK

[> Learn More](#)



Missed Xilinx Adapt? Watch the free on-demand replay

[> Register Free](#)



Run the Virtual DeepField-SR Video Super-Resolution Solution on the Xilinx App Store

[> Try Now](#)



Webinar: Designing AI Engines of Xilinx Versal ACAP Using Simulink & Vitis Model Composer

[> Register](#)



Developer Project of the Month Spotlight

[> Support Vector Machine a Zynq MPSoC Board](#)

Get Started



Get Started with Vitis

[> Learn More](#)



Get Started with Vivado ML

[> Learn More](#)



Get Started with Vitis AI

[> Learn More](#)

Hot Topic Developer Articles

- [Introduction Tutorial to the Vitis AI Profiler](#)
- [Bringing OFA \(Once-for-All\) to FPGA](#)
- [The Fifth Driver AI: Hardware-based accelerators for ADAS](#)
- [Implementing AI in an Embedded Project](#)
- [Xilinx App Store: The One-Stop Shop to Evaluate and Buy Alveo and Kria SOM Solutions](#)

developer.xilinx.com
Access Developer Resources
and How-to-Articles

[GET STARTED](#)



© 2021 [Xilinx](#) | [Privacy](#) | [Legal](#) | [Contact](#)
Xilinx Inc. 2100 Logic Dr, San Jose, CA 95124
[Manage your preferences](#) | [Unsubscribe](#)