



## **Xilinx Announces the World's Highest Performance Adaptive Devices for Advanced ADAS and AD Applications**

*Xilinx automotive-qualified line now scales from edge sensors to complex domain controllers*

**THE HAGUE, Netherlands (Xilinx Developer Forum Europe 2019), Nov. 12, 2019** – Xilinx, Inc., (NASDAQ: XLNX), the leader in adaptive and intelligent computing, today announced the expansion of its automotive-qualified 16 nanometer (nm) family with two new devices – the Xilinx® Automotive (XA) Zynq® UltraScale+™ MPSoC 7EV and 11EG. These two new parts deliver the highest programmable capacity, performance and I/O capabilities enabling high-speed, data aggregation, pre-processing, and distribution (DAPD), as well as compute acceleration for L2+ to L4 advanced driver-assistance systems (ADAS) and autonomous driving (AD) applications.

With these new additions, Xilinx now offers the world's highest level of silicon integration that meets the safety, quality and reliability requirements for automotive, with a comprehensive line of products scaling from small devices powering edge sensors to new high-performance devices for centralized domain controllers.

The new XA Zynq UltraScale+ MPSoC 7EV and 11EG devices were developed as a result of customer demand. The devices offer over 650,000 programmable logic cells – and nearly 3,000 DSP slices, which is 2.5X increase versus the previous largest device. In addition, the XA 7EV contains a video codec unit for h.264/h.265 encode and decode, while the XA 11EG includes 32 12.5Gb/s transceivers and provides four PCIe® Gen3x16 blocks. The addition of these high-performance devices to the XA portfolio enables carmakers, robotaxi developers, and Tier-1 suppliers to perform the DAPD and compute acceleration in a power envelope that allows for scalable production deployments for AD vehicles.

“The new additions to our automotive portfolio expand our proven offering to the market,” said Emre Onder, senior vice president, marketing, Xilinx. “Through customer-driven requests, we’ve

broadened our XA product family to meet the complex levels of today's ADAS and autonomous driving systems. With these additions to the Zynq UltraScale+ product line, Xilinx delivers unmatched processing flexibility and scalability vital for today's rapidly changing requirements. Whether customers are developing for L1 or for L4 systems, we have a solution that meets their needs."

The XA Zynq UltraScale+ MPSoC portfolio is qualified according to AEC-Q100 test specifications and integrates both Xilinx programmable logic and a feature-rich 64-bit quad-core Arm® Cortex®TM-A53 and dual-core Arm Cortex-R5 based processing system that is certified to ASIL-C level in the low power domain. The combination of these features, paired with the high data throughput capability of the new devices, accelerate the deployment of autonomous vehicles on the road today. To date, Xilinx has shipped more than 67 million auto-qualified solutions for ADAS systems and AD production vehicles to over 200 automotive companies including global Tier-1 suppliers, OEMs and start-ups.

The new XA Zynq UltraScale+ MPSoC devices are available for order today. Full technical details on the XA family can be found [here](#). All XA devices are supported by [Vitis™](#) and Vitis AI, the company's new unified software platform that enables a broad range of developers to take advantage of the power of hardware adaptability.

For more information, visit: <https://www.xilinx.com/products/silicon-devices/soc/xa-zynq-ultrascale-mpsoc.html>.

### **About Xilinx**

Xilinx develops highly flexible and adaptive processing platforms that enable rapid innovation across a variety of technologies – from the endpoint to the edge to the cloud. Xilinx is the inventor of the FPGA, hardware programmable SoCs, and the ACAP, designed to deliver the most dynamic processor technology in the industry and enable the adaptable, intelligent, and connected world of the future. For more information, visit [www.xilinx.com](http://www.xilinx.com).

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