



Circuit Revision Change to the CoolRunner-II Family of CPLD Devices

PCN2004-24 (v1.2) August 19, 2005

Product/Process Change Notice - FYI

Overview

The purpose of this notification is to communicate a circuit revision change to the CoolRunner™-II family of CPLD devices.

Description

As part of the Xilinx continuous improvement process, a new circuit revision is being released for the CoolRunner-II family of CPLD devices. This circuit revision increases the tolerance of the configuration circuitry to non-monotonic power supply ramps and provides a more robust solution in customer applications. This new revision is backward compatible to the material that is currently shipping in production.

Products Affected

This change affects all package, temperature, and speed grade combinations of the following CoolRunner-II devices: XC2C128, XC2C256, XC2C384, and the XC2C512.

Programming and Test Software

If you are using Xilinx iMPACT software, a BP Microsystems programmer, or a Systems General programmer to program these CoolRunner-II devices, no action is required.

For customers using BSDL files for programming and/or board testing of these CoolRunner-II devices, please download files of revision 1.1 or later from http://www.xilinx.com/xlnx/xil_sw_updates_home.jsp. Earlier versions of BSDL files had the revision number hard coded into the file, whereas the newer files have marked the revision field as a "don't care". Therefore, using the old version of a BSDL file will give IDCODE errors if used with a newer CoolRunner-II device.

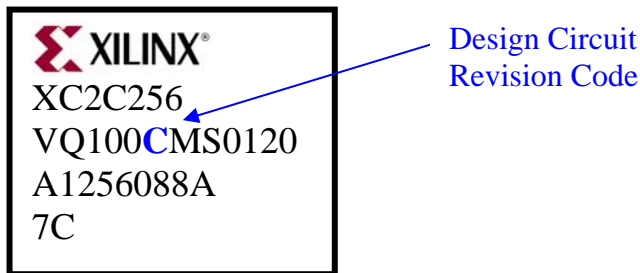
Key Dates

The new XC2C128 and XC2C256 circuit revision codes are currently in production. Additional family members will be released to production per the table below. Customers should expect to receive devices of both circuit revisions until inventory of the earlier circuit revision is depleted. The new design circuit revision codes by family member are as follows:

Family Member	Circuit Revision Code	Production Availability
XC2C128	B	June 2005
XC2C256	C	October 2004
XC2C384	B	December 2005
XC2C512	B	September 2005

Traceability

The design circuit revision code on the package topmark can be found as shown below:



Recommendation

No response is required to this PCN. For additional information or questions, please contact [Xilinx Technical Support](#).

Important Notice: Xilinx Customer Notifications (PCN, PDN, and Quality Alerts) can be delivered via e-mail alerts sent by the MySupport website (<http://www.xilinx.com/support>). Register today and personalize your "MyAlerts" to include Customer Notifications. This change provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, and so forth. For instructions on how to sign up, refer to [Xilinx Answer Record 18683](#).

Revision History

The following table shows the revision history for this document.

Date	Version	Revision
10/4/2004	1.0	Initial release.
12/13/2004	1.1	Added additional family members and their availability dates.
8/19/2005	1.2	Updated availability dates and added programming and test software information. Updated link to BSDL files.