

Overview

The purpose of this notice is to inform Virtex™-4 FX users of a revision to the PowerPC™ operating frequency for designs using the APU Controller.

Description

By design, the APU Controller operates at the same frequency as the PowerPC processor. Silicon characterization has shown a performance limitation when operating the APU Controller and PowerPC at the frequencies specified in the latest errata. When using the APU Controller, the operating frequency of the PowerPC must be reduced.

Products Affected

This change affects all Virtex-4 FX devices.

Table 1: PowerPC Operating Frequency for Designs Using the APU Controller

Virtex-4 FX PowerPC / APU Operating Frequencies		
Speed Grades	Original Frequency	Revised Frequency
-10	275 MHz	233 MHz
-11	325 MHz	275 MHz
-12	350 MHz	333 MHz

Response

No response is required. For additional information or questions, please contact [Xilinx Technical Support](#).

Important Notice: Xilinx Customer Notifications (PCN, PDN, and Quality Alerts) can be delivered via e-mail alerts sent by the MySupport Web site (<http://www.xilinx.com/support/mysupport.htm>). Register today and personalize your “MyAlerts” to include Customer Notifications. This change provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, and so forth. For instructions on how to sign up, refer to [Xilinx Answer Record 18683](#).

Revision History

The following table shows the revision history for this document.

Date	Version	Revision
11/13/06	1.0	Initial release.