



Revised I_{CCA} Specification for XC1701 and XC17S40 PROM Devices

XCN07011 (v1.0) July 9, 2007

For Your Information

Overview

The purpose of this notice is to inform XC1701 and XC17S40 family customers of a revision to the worst case I_{CCA} active mode supply current specification. This revision will not require any changes to a customer's end system.

Description

The I_{CCA} specification for the XC1701 and XC17S40 product is being updated from 10 mA to 20 mA in DS027 and DS030, respectively to more accurately reflect the operation of the device. This is a test program change. No change has been made to the device or process technology. The XC1701 change has been added to the [DS027](#) (v3.4) data sheet, and the XC17S40 change has been added to the [DS030](#) (v1.11) data sheet.

This I_{CCA} specification is relevant only during FPGA configuration and has no impact to customers using XC1701 and XC17S40 devices as FPGA configuration devices. When used as configuration devices, the FPGA and PROM share a common power supply rail. The FPGA power requirement during system operation is at least five times greater than the power requirement of the FPGA and PROM during configuration. Therefore, this revision will not cause any changes to a customer's end system.

Products Affected

This change affects only the XC1701 and XC17S40 PROM devices.

Table 1: Changes to the DC Operating Conditions

I_{CCA} Characteristics Over Operating Condition		
Device	Original I_{CCA}	Revised I_{CCA}
XC1701	10 mA (max)	20 mA (max)
XC17S40	10 mA (max)	20 mA (max)

Response

No response is required. For additional information or questions, please contact [Xilinx Technical Support](#).

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Revision History

The following table shows the revision history for this document.

Date	Version	Revision
07/09/07	1.0	Initial Xilinx release.