

## Overview

The purpose of this notification is to communicate that Xilinx is discontinuing certain Development Systems Products. This notice is an early notice to all existing customers and field sales.

## Description

As part of standard Xilinx product pruning activities, the following products have been identified for obsolescence. This notice should be used in conjunction with previous discontinuance notices which can be found at:

[http://www.xilinx.com/support/documentation/customer\\_notices.htm](http://www.xilinx.com/support/documentation/customer_notices.htm).

## Products Affected

*Table 1: LogiCORE™, Fibre Channel IP*

Xilinx Part Number	Description	Xilinx Replacement
EF-DI-FC-SITE	LogiCORE, Fibre Channel, Site License	No Direct Xilinx Replacement*
EFR-DI-FC-SITE	LogiCORE, Fibre Channel, Site License Renewal	No Direct Xilinx Replacement*

\* For a Third Party solution, please contact Xilinx Alliance Partner [MorethanIP](#) for information on their new 8G/4G/2G/1G Fibre Channel IP core.

## Key Dates and Ordering Information

Final orders are accepted until March 31st, 2010.

Final deliveries must occur on or before April, 15th, 2010.

No technical support available after March 31st, 2011.

Final update to this core (v3.5) will be released in ISE<sup>®</sup> 12.1 in CY10Q2. To access this new version when it becomes available, you must install the 12.1 release and click the "All IP Versions" checkbox when you invoke ISE CORE Generator™ as it will be hidden by default.

## Response

Customers are requested to place last-time-buy orders in a timely manner prior to the key dates listed above to avoid product availability issues. If you anticipate that your last time buy requirements will exceed the current rate, please provide your Xilinx Sales Representative with a forecast of the requirements. All orders will be supported on a first-come, first-served basis with the non-cancellable and non-returnable (NCNR) condition. For additional information or questions, contact your local Xilinx Sales representative.

**Important Notice:** Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the MySupport website (<http://www.xilinx.com/support>). Register today and personalize your "MyAlerts" area to include Customer Notifications. Xilinx MySupport provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to [Xilinx Answer Record 18683](#).

## Revision History

The following table shows the revision history for this document:

Date	Version	Description of Revisions
2/08/10	1.0	Initial release.
04/08/10	1.0.1	Minor typo correction on the footer.

## Notice of Disclaimer

THE XILINX HARDWARE FPGA AND CPLD DEVICES REFERRED TO HEREIN (“PRODUCTS”) ARE SUBJECT TO THE TERMS AND CONDITIONS OF THE XILINX LIMITED WARRANTY WHICH CAN BE VIEWED AT <http://www.xilinx.com/warranty.htm>. THIS LIMITED WARRANTY DOES NOT EXTEND TO ANY USE OF PRODUCTS IN AN APPLICATION OR ENVIRONMENT THAT IS NOT WITHIN THE SPECIFICATIONS STATED ON THE XILINX DATA SHEET. ALL SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE. PRODUCTS ARE NOT DESIGNED OR INTENDED TO BE FAIL-SAFE, OR FOR USE IN ANY APPLICATION REQUIRING FAIL-SAFE PERFORMANCE, SUCH AS LIFESUPPORT OR SAFETY DEVICES OR SYSTEMS, OR ANY OTHER APPLICATION THAT INVOKES THE POTENTIAL RISKS OF DEATH, PERSONAL INJURY OR PROPERTY OR ENVIRONMENTAL DAMAGE (“CRITICAL APPLICATIONS”). USE OF PRODUCTS IN CRITICAL APPLICATIONS IS AT THE SOLE RISK OF CUSTOMER, SUBJECT TO APPLICABLE LAWS AND REGULATIONS. ALL SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE.