

Overview

The purpose of this notification is to communicate a new shipping tray for the Xilinx VO48/VOG48 packages.

Description

Xilinx is changing the shipping tray from Daewon to Kostat vendor for the VO48/VOG48 packages.

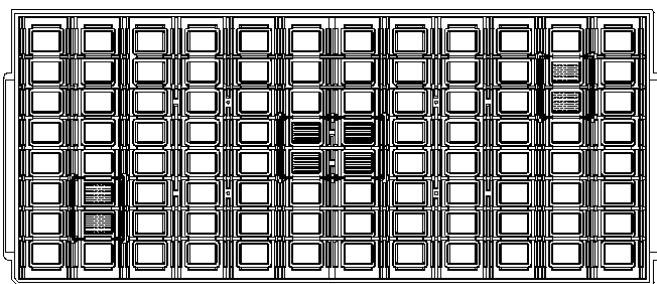
The new Kostat tray matrices are designed to be compatible with the current Daewon shipping trays, and will have the same X/Y tray dimensions. However, the Kostat trays are non-stackable with Daewon tray due to a slip lock feature. The new trays have deeper nesting pockets (1.91mm vs 1.50mm deep) that more securely capture the components, resulting in less jostling related incidents. Xilinx recommends that customers not stack or mix the new Kostat trays with the Daewon trays.

Xilinx will begin using the new shipping tray starting September 9th, 2012 (Date code: 1237).

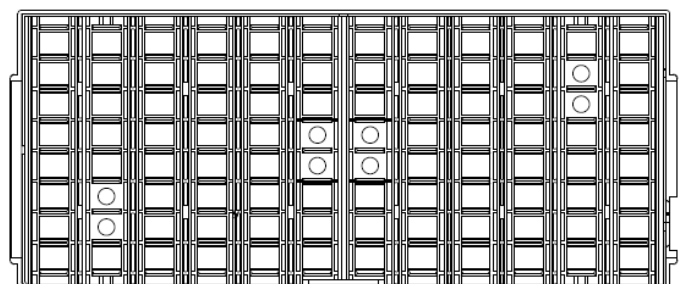
Please reference [Table 1](#) and [Figure 1](#) for clarification:

Table 1: Tray Information

Tray Description	Current Tray		New Tray	
	Daewon Tray Part Number	Xilinx Drawing	Kostat Tray Part Number	Xilinx Drawing
12X20 TSOP THIN MATRIX TRAY (96 pockets) for VO48/VOG48 pkg	T1007005	SIT0141	KS-84018-6120	SIT0144



Current Tray: T1007005
Vendor: Daewon



New Tray: KS-84018-6120
Vendor: Kostat

Figure 1: Tray Details

Products Affected

This change affects all VO48/VOG48 packages, in any temperature or grade offering. Affected part numbers are included in the following [Table 2](#):

Table 2: Affected Devices and Packages

Device	Device
XCF08PVOG48C	XCF32PVOG48C
XCF08PVOG48C0936	XCF32PVOG48C0936
XCF16PVOG48C	XQF32PVOG48M
XCF16PVOG48C0936	XQF32PVO48M

Response

No response is required. For additional information or questions, please contact [Xilinx Technical Support](#).

Important Notice: Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the Support website (<http://www.xilinx.com/support>). Register today and personalize your “Documentation and Design Advisory Alerts” area to include Customer Notifications. Xilinx Support provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to Answer Record 18683: <http://www.xilinx.com/support/answers/18683.htm>.

Revision History

The following table shows the revision history for this document:

Date	Version	Description of Revisions
04/30/12	1.0	Initial release.

Notice of Disclaimer

The information disclosed to you hereunder (the “Materials”) is provided solely for the selection and use of Xilinx products. To the maximum extent permitted by applicable law: (1) Materials are made available “AS IS” and with all faults, Xilinx hereby DISCLAIMS ALL WARRANTIES AND CONDITIONS, EXPRESS, IMPLIED, OR STATUTORY, INCLUDING BUT NOT LIMITED TO WARRANTIES OF MERCHANTABILITY, NON-INFRINGEMENT, OR FITNESS FOR ANY PARTICULAR PURPOSE; and (2) Xilinx shall not be liable (whether in contract or tort, including negligence, or under any other theory of liability) for any loss or damage of any kind or nature related to, arising under, or in connection with, the Materials (including your use of the Materials), including for any direct, indirect, special, incidental, or consequential loss or damage (including loss of data, profits, goodwill, or any type of loss or damage suffered as a result of any action brought by a third party) even if such damage or loss was reasonably foreseeable or Xilinx had been advised of the possibility of the same. Xilinx assumes no obligation to correct any errors contained in the Materials, or to advise you of any corrections or update. You may not reproduce, modify, distribute, or publicly display the Materials without prior written consent. Certain products are subject to the terms and conditions of the Limited Warranties which can be viewed at <http://www.xilinx.com/warranty.htm>; IP cores may be subject to warranty and support terms contained in a license issued to you by Xilinx. Xilinx products are not designed or intended to be fail-safe or for use in any application requiring fail-safe performance; you assume sole risk and liability for use of Xilinx products in Critical Applications: <http://www.xilinx.com/warranty.htm#critapps>.