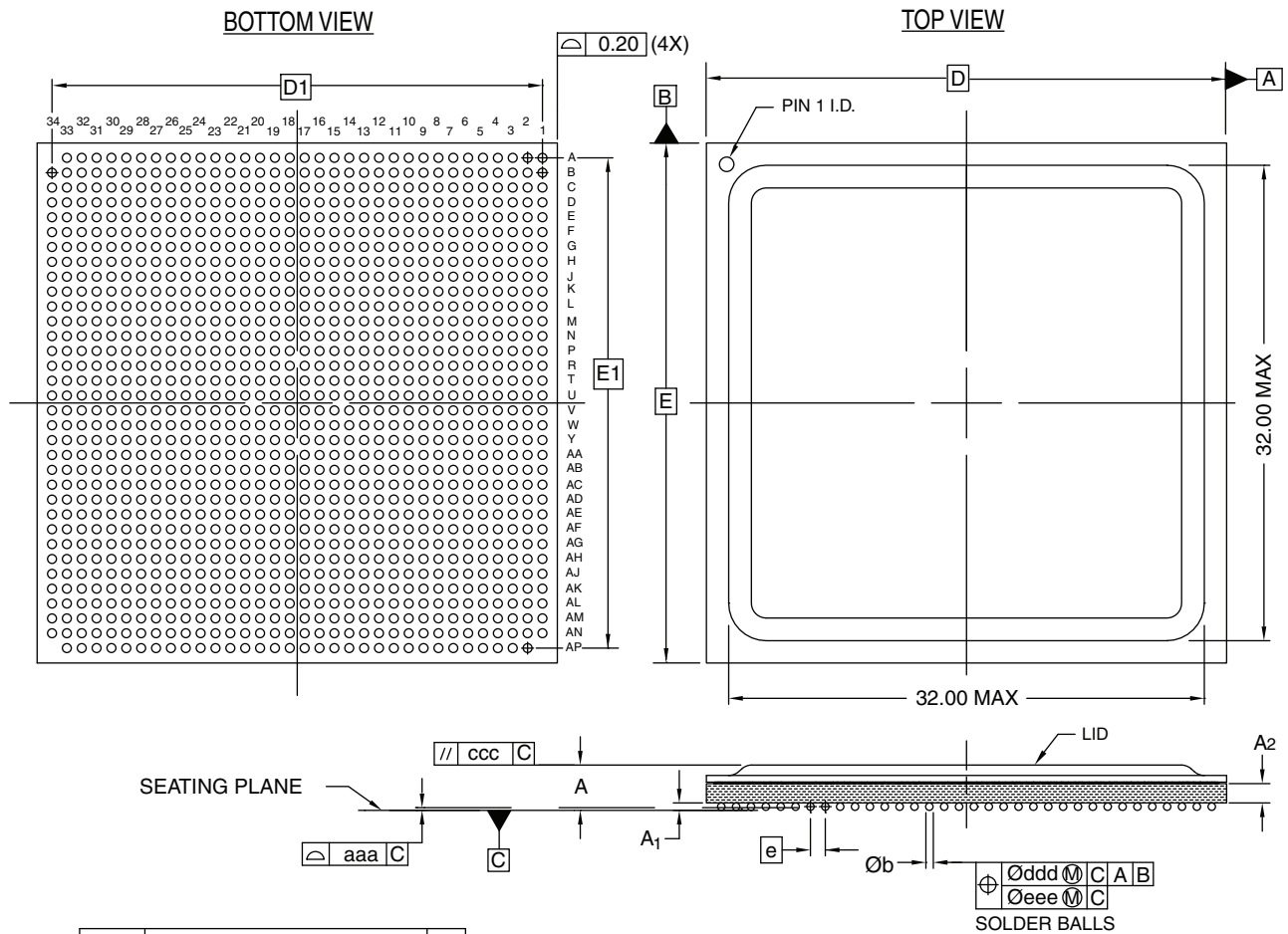


# Flip-Chip BGA (FF1153) Package



### NOTES:

1. ALL DIMENSIONS AND TOLERANCES CONFORM TO ANSI Y14.5M-1994
2. SYMBOL 'M' IS THE BALL MATRIX SIZE.
3. CONFORMS TO JEDEC MS-034-AAR-1 (DEPOPULATED)

## 1153-BALL FLIP CHIP BGA (FF1153/FFG1153)

## Revision History

The following table shows the revision history for this document.

Date	Version	Revision
05/15/06	1.0	Initial Xilinx release.
12/15/08	1.1	Corrected symbol A2 dimension.