

KINTEX-7Q DEFENSE-GRADE FPGA



		Kintex™-7Q FPGAs	
		XQ7K325T	XQ7K410T
Logic Resources	Part Number		
	Slices	50,950	63,550
	Logic Cells	326,080	406,720
Memory Resources	CLB Flip-Flops	407,600	508,400
	Maximum Distributed RAM (Kb)	4,000	5,663
	Block RAM/FIFO w/ ECC (36 Kb each)	445	795
Clock Resources	Total Block RAM (Kb)	16,020	28,620
	Mixed Mode Clock Managers (MMCM)	10	10
I/O Resources	Maximum Single-Ended I/O	500	500
	Maximum Differential I/O Pairs	240	240
Embedded Hard IP Resources	DSP48E1 Slices	840	1,540
	PCI Express® ⁽¹⁾	1	1
	Analog Front End (XADC) / SysMon Blocks	1	1
	Configuration AES / HMAC Blocks	1	1
	GTX 10.3125 Gb/s Transceivers	16	16
Speed Grades	Extended Temp (0° to +100°C)	-2L	-2L
	Industrial Temp (-40° to +100°C)	-1, -2, -2L	-1, -2, -2L
	Military Temp (-55° to +125°C)	-1, -1L ⁽²⁾	-1
	Package⁽³⁾	Dimensions (mm)	Available User I/O: 3.3V SelectIO™ Pins, 1.8V SelectIO Pins (GTX Transceivers)
	RF676	27 x 27	250, 150 (8)
	RF900	31 x 31	350, 150 (16)

XMP090 (v1.1)

- Notes:**
1. Supports PCI Express Base 2.1 specification at Gen1 and Gen2 data rates.
 2. The -1L is only available in the RF676 package for this device.
 3. This is *preliminary product information, subject to change*. Please contact A&D Marketing for the latest information.

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