

Libraries Guides

The various *Libraries Guides* contain information about the Xilinx Unified Libraries design elements, including macros and primitives. Each guide targets a specific device family and design entry method, and covers the following:

- Design entry methods
- Functional categories for design elements
- Design element information

Note HDL guides also contain instantiation code that you can copy and paste into your projects.

The following guides are available from the [Libraries Guides](#) page of the Xilinx® website:

- [CPLD Libraries Guide \(UG606\)](#)
- [Spartan®-3 Libraries Guide for HDL Designs \(UG607\)](#)
- [Spartan-3 Libraries Guide for Schematic Designs \(UG608\)](#)
- [Spartan-3A and Spartan-3A DSP Libraries Guide for HDL Designs \(UG613\)](#)
- [Spartan-3A and Spartan-3A DSP Libraries Guide for Schematic Designs \(UG614\)](#)
- [Spartan-3E Libraries Guide for HDL Designs \(UG617\)](#)
- [Spartan-3E Libraries Guide for Schematic Designs \(UG618\)](#)
- [Spartan-6 Libraries Guide for HDL Designs \(UG615\)](#)
- [Spartan-6 Libraries Guide for Schematic Designs \(UG616\)](#)
- [Virtex®-4 Libraries Guide for HDL Designs \(UG619\)](#)
- [Virtex-4 Libraries Guide for Schematic Designs \(UG620\)](#)
- [Virtex-5 Libraries Guide for HDL Designs \(UG621\)](#)
- [Virtex-5 Libraries Guide for Schematic Designs \(UG622\)](#)
- [Virtex-6 Libraries Guide for HDL Designs \(UG623\)](#)
- [Virtex-6 Libraries Guide for Schematic Designs \(UG624\)](#)
- [Xilinx 7 Series Libraries Guide for HDL Designs \(UG768\)](#)
- [Xilinx 7 Series Libraries Guide for Schematic Designs \(UG799\)](#)