

REGISTRATION FORM

Name: _____

Qualification: _____

Designation: _____

Department: _____

Institute: _____

Contact Details

Postal Address:

Telephone: _____

E mail: _____

Payment Details: _____

D.D amount: _____

In favor of: Director, VJTI

Payable at: Mumbai

D.D no.: _____

Bank: _____

Whether accommodation needed: Yes/No

Signature of Applicant: _____

Date: _____

Place: _____

The registration is open for faculty members from engineering colleges, industrial professionals, PG students & research scholars.

Registration charges per participant:

Academician: ₹ 3000/-

Industry Delegates: ₹ 5000/-

M.Tech/PhD/Research Students: ₹ 500/-

***Only 35 participants will be selected on first come first service basis. Intimation of selection will be done via e-mail.**

Address of correspondence to send hard copy of registration form:

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Email-nishasarvade@vjti.org.in

Mumbai

Ph: 02224198195.



**Department of Electrical Engineering
VJTI, Mumbai.
In Collaboration with**



Is pleased to announce a

Two day Faculty Development Workshop on

**“Embedded System Design
targeting Zed board using
Xilinx Vivado”**

On

10th & 11th December, 2014

Co-Coordinator: Dr. Nisha Sarwade

About faculty development on “Embedded System Design targeting Zed board using Xilinx Vivado”

Course Description

This course provides participants with an introduction to embedded system design flow on Zynq using ZedBoard and Xilinx Vivado® design software suite.

Pre-requisites

Digital logic and FPGA design experience

- Basic experience with Xilinx Vivado design software suite
- Basic understanding of C programming
- Basic microprocessor experience

Course Schedule

Day 1: Introduction to Embedded System Design using Zynq

Lab 1: Simple Hardware Design

- Create a Vivado project and use IP Integrator to develop a basic embedded system for a target board.
- Zynq Architecture
- Extending the Embedded System into Programmable Logic

Lab 2: Adding Peripherals in Programmable Logic

- Extend the hardware system by adding AXI peripherals from the IP catalog.
- Adding Your Own IP Peripheral

Lab 3: Creating & Adding Your Own Custom IP

- Use the Manage IP feature of Vivado to create a custom IP and extend the system with the custom peripheral.

Day 2: Software Development Environment

Lab 4: Writing Basic Software Applications

- Write a basic C application to access the peripherals.
- Software Development and Debugging

Lab 5: Software Debugging Using SDK

- Use API to drive CPU's timer. Perform software debugging using SDK.

About CoreEL Technologies

CoreEL Technologies is a technology company with Development, Distribution and Training. CoreEL is a leading provider of VLSI Embedded System design services and intellectual Property. An authorized distributors of Mentor Graphics products in India with a dedicated team of Industry and Academic/Research oriented customers.

About Speaker

Name of speaker: Dr. Parimal Patel

Dr. Parimal Patel received his Doctor of Philosophy in Electrical and Computer engineering from University of Texas at Austin, Texas, USA in 1986. In 1987, he joined University of Texas, as assistant professor, got promoted to Associate and then to full Professorships. During his tenure at the university, he taught variety of courses including Logic Design, Digital System Design, Microcomputer systems (Peripheral Interface Principles), Embedded system Design, VLSI System design, Computer Architecture, RISC Processor Design, Engineering Workstations & Advanced HDL Modeling. He has always enjoyed teaching and developing new courses. He started as a contract trainer and

then full time employee of Xilinx developing variety of courses, updating current courses & delivering XUP workshops worldwide.

About VJTI, Mumbai

Veermata Jijabai Technological Institute was given its present title on January 26, 1997. Founded in 1887, it was then known as Victoria Jubilee Technical Institute. The institute was granted financial and academic autonomy from June 21, 2004. Under the World Bank grant - TEQIP - the institute has several projects underway to establish itself as a world class technological institute. The modernization will offer excellent central computing facilities to students, who will also have access to improved laboratory and workshop facilities. The faculty at the institute is recognized for its Testing and Consultancy work. Realizing the potential of the institute, reputed industries have sponsored laboratories - for instance, the high voltage laboratory, courtesy Siemens; and the Computational Fluid Dynamics laboratory, courtesy B.A.R.C.

About Department of Electrical Engineering:

Year of establishment: 1900 (Started with Diploma in Electrical Engineering). It is recognized for doctoral research heritage and competent faculty members in many diverse fields. It has developed state of art - High Voltage Laboratory due to generous funding by Siemens (Germany) and All India Council for Technical Education (AICTE). This laboratory caters to various testing needs of industry, carries out consultancy & research for industries and Government organizations. The Department has recently developed state of art VJTI- L&T Low Voltage Switchgear Laboratory to bridge the gap between industry and Academics