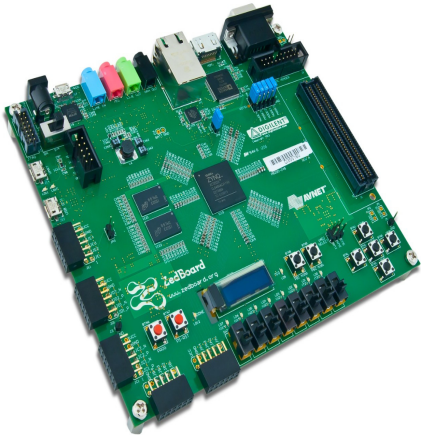


HARDWARE PLATFORM

ZedBoard Zynq-7000 Development Board



Features:

- Zynq-7000 EPP XC7Z020-CLG484
- Memory:
 - 512 MB DDR3
 - 256 Mb Quad-SPI Flash
 - 4 GB SD card
- Onboard USB-JTAG Programming
- 10/100/1000 Ethernet
- USB OTG 2.0 and USB-UART
- PS & PL I/O expansion (FMC, Pmod™, XADC)
- Multiple displays (1080p HDMI, 8-bit VGA, 128 x 32 OLED)
- I²S Audio CODEC

ORGANIZING COMMITTEE

Patron

Dr.M.Jayakumar (Chairperson ECE)

Convener

Dr.S.Veni (Associate Professor , ECE)

Coordinator

Mr.Peeyush.K.P (Assistant Professor, ECE)

Co-coordinators

Mr.J.Aravinth (Assistant Professor, ECE)

Mr.M.Ganesan (Assistant Professor, ECE)

RESOURCE PERSONS

Ms.Sadiya, National Manager

Mr.Balachander.H, Manager,FAE

Mr.Vidyasagar M.K

vidyasagar.mk@coreel.com

University Program, CoreEL Technologies,
Bangalore. [Tel:09972066855](tel:09972066855).

For further enquiries and communication,

Co-ordinator

**National Workshop on
EMBEDDED DESIGN FLOW
USING**

XILINX ZYNQ SoC

Department of ECE

Amrita Vishwa Vidyapeetham

Amritanagar(P.O), Ettimadai

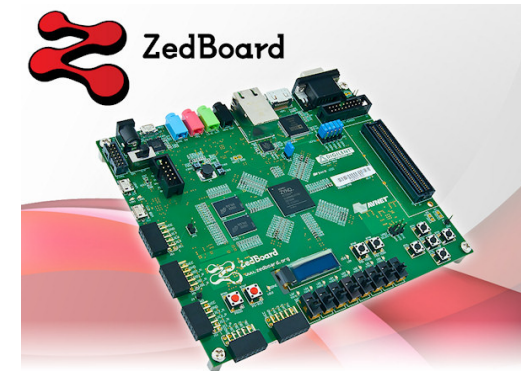
Coimbatore - 641 112

Ph. 0422 2685000 / 5721

Mobile: 9626947031 / 9566772677

National Workshop on Embedded Design Flow using XILINX ZYNQ SOC

February 27th & 28th, 2015



Organized by

Department of
Electronics and Communication Engineering



In Association with



About Us

Amrita Vishwa Vidyapeetham established under the guidance of Satguru Mata Amritanandamayi Devi, with its head quarters at Ettimadai, Coimbatore is one of the youngest educational institutions in India to have been granted the University status. Amrita Vishwa Vidyapeetham is one of the very few universities to have a multi-campus, multi-disciplinary character, offering under graduate, post graduate and research programmes.. Amrita is the nation's first multi-campus university interlinked via satellite facilitating E-learning that transcends limitations of time & space. NAAC has accredited A Grade to Amrita University..

About CoreEL Technologies

CoreEL Technologies (I) Pvt Ltd, CoreEL is a customer Application Specific Products & Solutions company offering Intellectual Property (IP) Hardware, Software & Engineering Services to customers, enabling them to Design Manufacture and Market world class electronic products. The portfolio of offerings include IP cores, System Design, Architecture, Validation, Sustenance, Prototype Manufacturing, Next-Gen products, Semiconductor solutions & Distribution of EDA Tools & COTS products. CoreEL; founded in 1999, an ISO 9001:2008 certified headquartered at Bangalore India.

About CoreEL University Program

CoreEL University Program provides Eco-System support to Indian Academia in Engineering Higher Education, in the field of Embedded Systems thereby enabling the delivery of quality education. CoreEL achieves this by providing state of the art products from **XILINX, MENTOR Graphics, MATLAB, ANSYS, VxWorks, Speedgoat** (RCP,HIL simulation & development), **PCB Design Tools** from Mentor, **VLSI, Embedded Students Training, Faculty Training** etc., with multiyear application engineering support on these products, faculty & student training, providing industry specific inputs to update the curriculum & helping Universities set up Centers of Excellence in Embedded Systems.

ELIGIBILITY

Faculty from AICTE approved Engineering Colleges with relevant background. Candidates from industries and R & D organizations will also be considered.PG students in related discipline are also eligible.

BOARDING AND LODGING

Boarding and lodging will be provided on payment basis, to the participants in university hostels inside the college campus on request.

REGISTRATION DETAILS

Registration for the course can be made by sending the duly filled Registration form along with a DD for **Rs.1500/- in favour of “Amrita School of Engineering”**, payable at Coimbatore. Registration Fee includes working lunch, tea breaks and required materials.

HOW TO APPLY

- 1) **Demand Draft for Rs.1500** should be drawn in favour of **“Amrita School of Engineering”**, payable at Coimbatore.
- 2) Fill in the Online Registration Form available at www.amrita.edu/site/esws.
- 3) Send the scanned copy of your DD along with a covering letter duly signed by the Principal of your institution to **“amritaeecews@gmail.com”** on or before February 20, 2015.
- 4) Sent original DD to the Coordinator of the Workshop. (address mentioned in the front page of the brochure) on or before February 20, 2015.

SCHEDULED DATES

Last date for online Reg. : 20th Feb, 2015

Intimation of selection : 23th Feb, 2015

Course Content

Day 1:

- Introduction to Embedded System Design using Zynq
- **Lab 1: Simple Hardware Design**
 - Create a Vivado project and use IP Integrator to develop a basic embedded system for a target board.
- Zynq Architecture
- Extending the Embedded System into Programmable Logic
- **Lab 2: Adding Peripherals in Programmable Logic**
 - Extend the hardware system by adding AXI peripherals from the IP catalog.
- Adding Your Own IP Peripheral
- **Lab 3: Creating and Adding Your Own Custom IP**
 - Use the Manage IP feature of Vivado to create a custom IP and extend the system with the custom peripheral.

Day 2:

- Software Development Environment
- **Lab 4: Writing Basic Software Applications**
 - Write a basic C application to access the peripherals.
- Software Development and Debugging
- **Lab5:** Advanced software writing
- **Lab 6:** Cross debug